ABSTRACT OF THE DISCLOSURE

A PWM signal generator and generating method for generating one or two pulses having a pulse width or a total pulse width corresponding to a value represented by a pulse code modulation digital signal and placed in a symmetric positional relationship with respect to the position of one half of a predetermined length. A first pulse and a second pulse are generated in accordance with a value represented by the digital signal. The difference in pulse width between the first and second pulses is output as a first pulse width modulation signal. When the value represented by the digital signal is zero, the first pulse and second pulse are equal in pulse width. When the value represented by the digital signal changes by one, one of the first and second pulses does not change in pulse width, while the other of the first and second pulse changes in pulse width by two slots.